

APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		

5889976

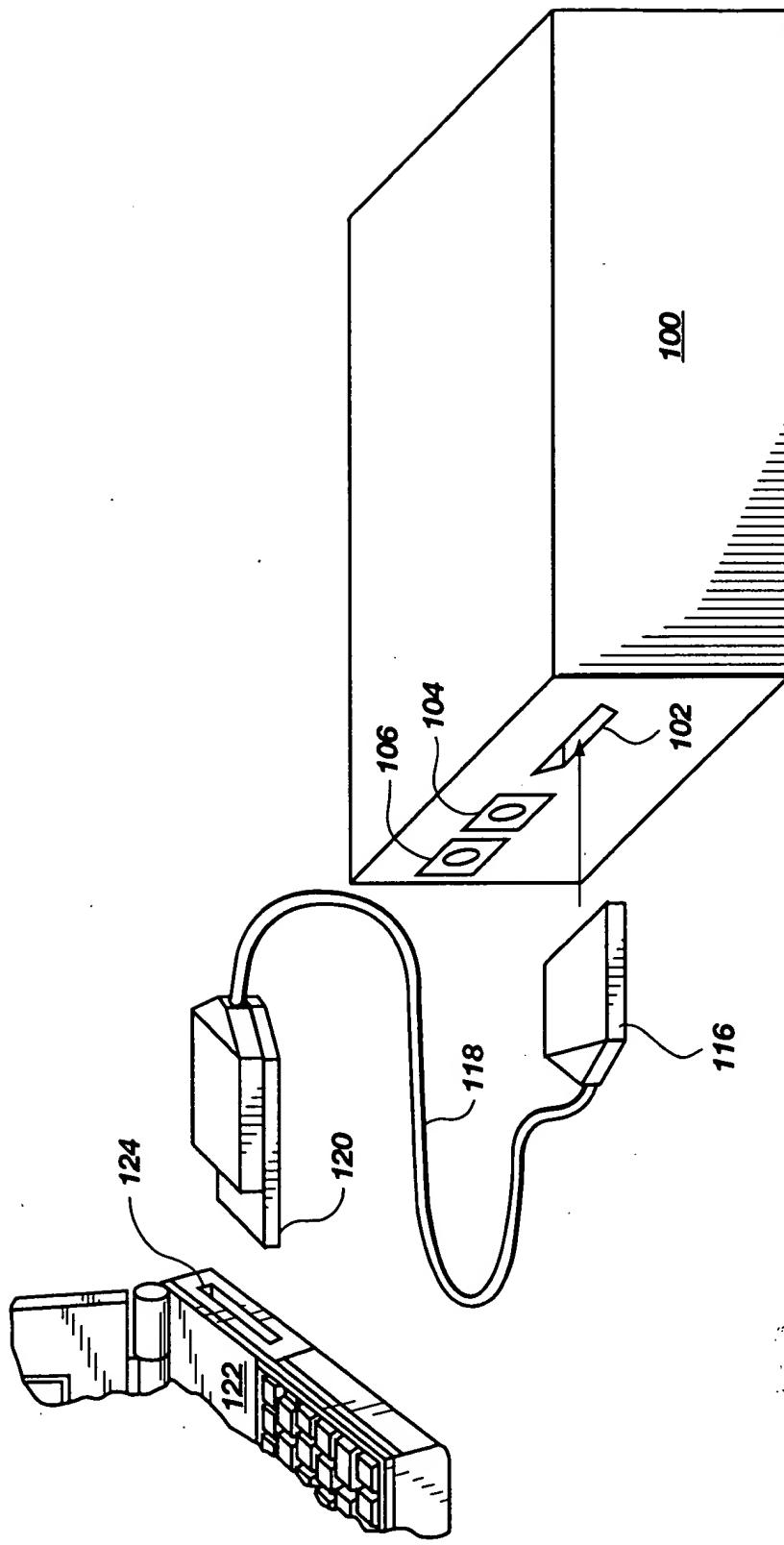


Fig. 1A

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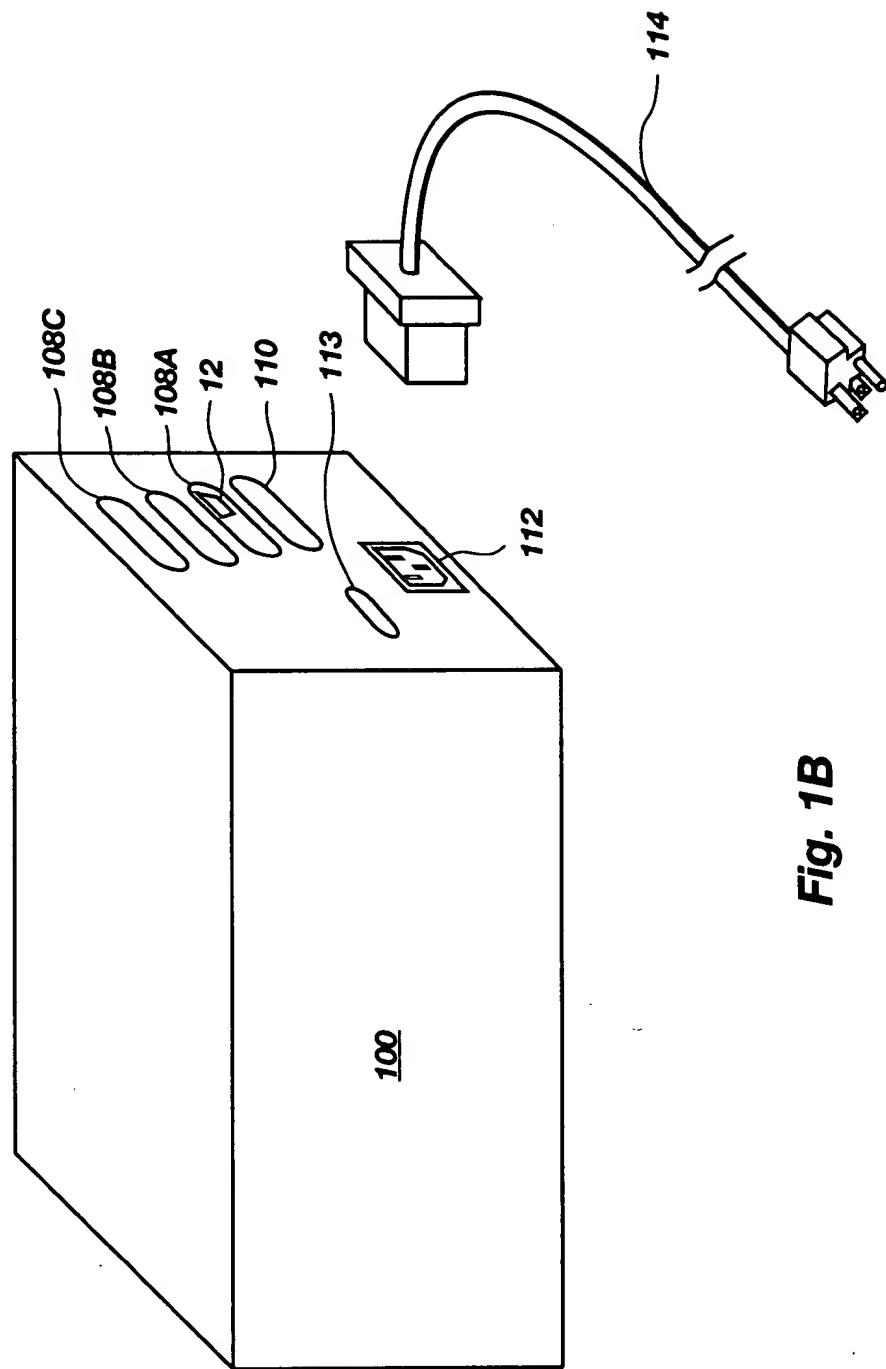
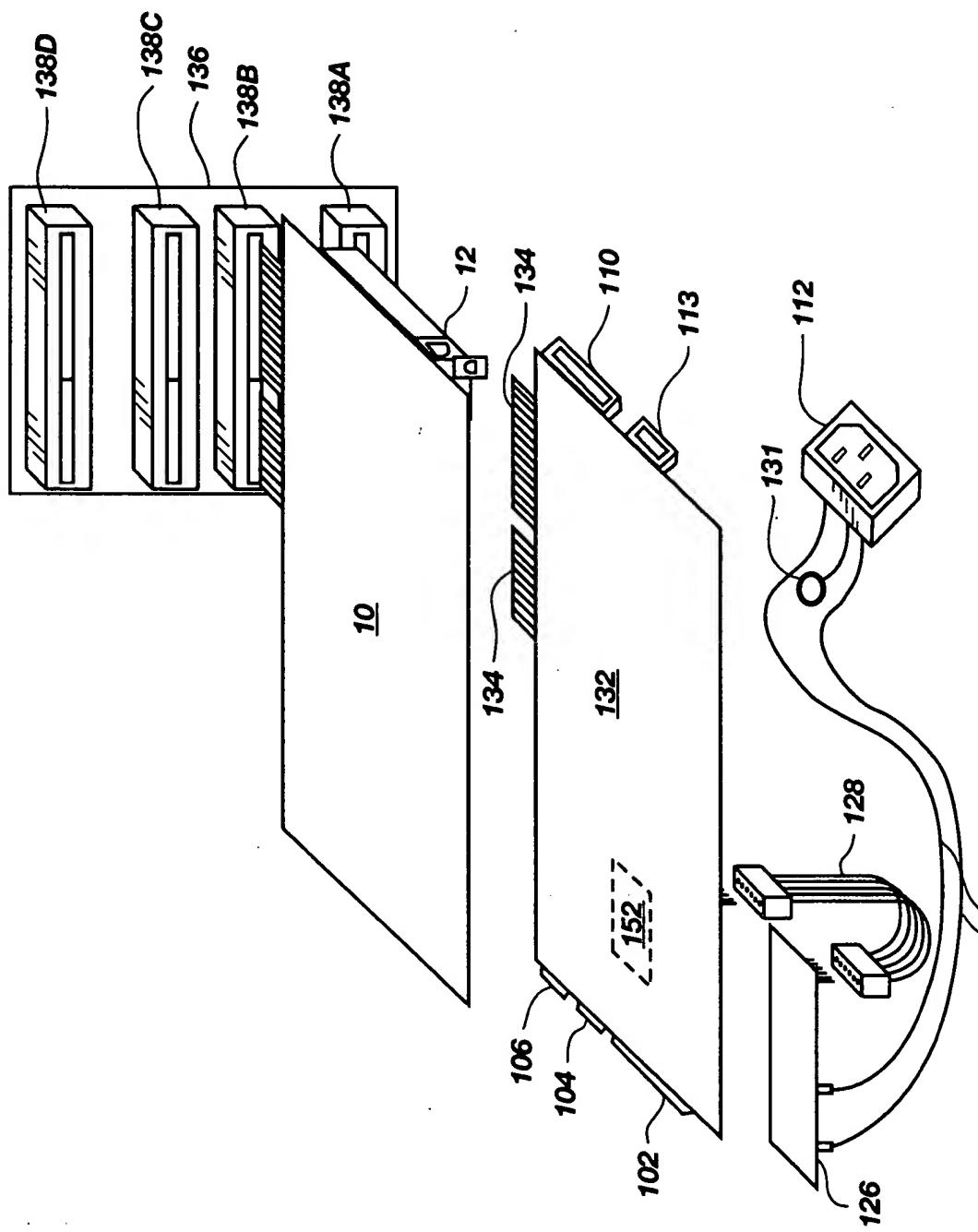


Fig. 1B

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	SUBCLASS

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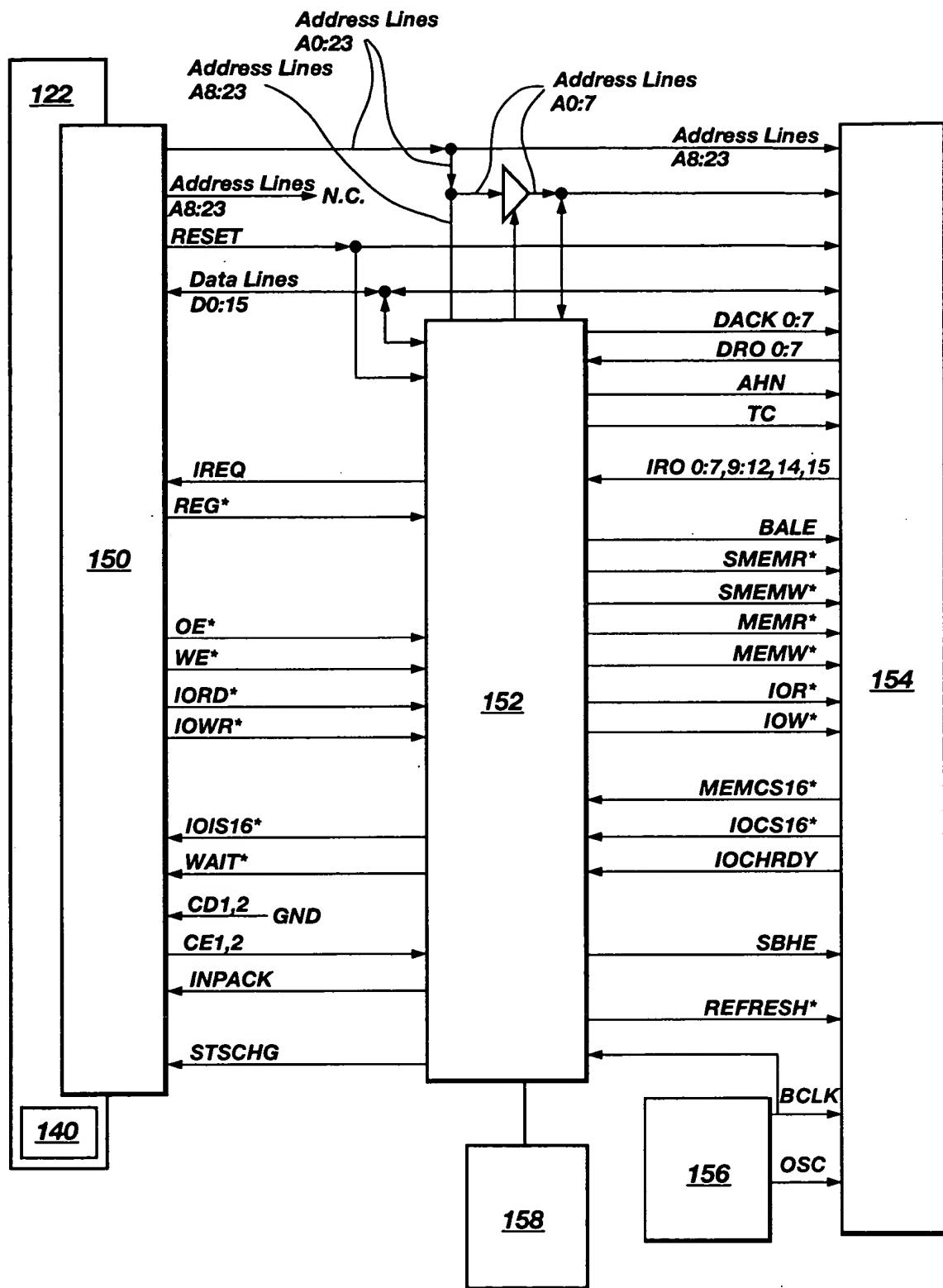


Fig. 3

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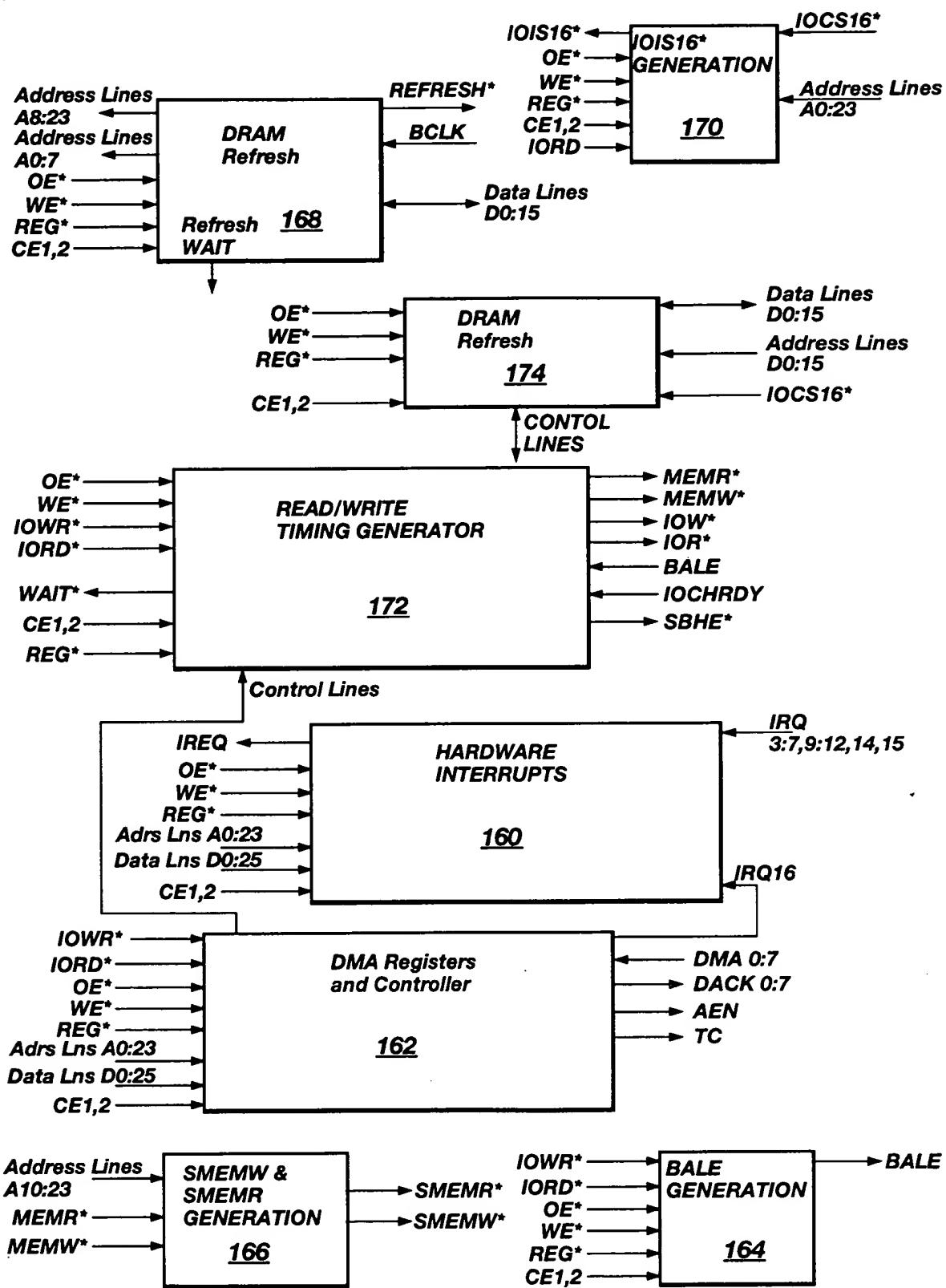


Fig. 3A

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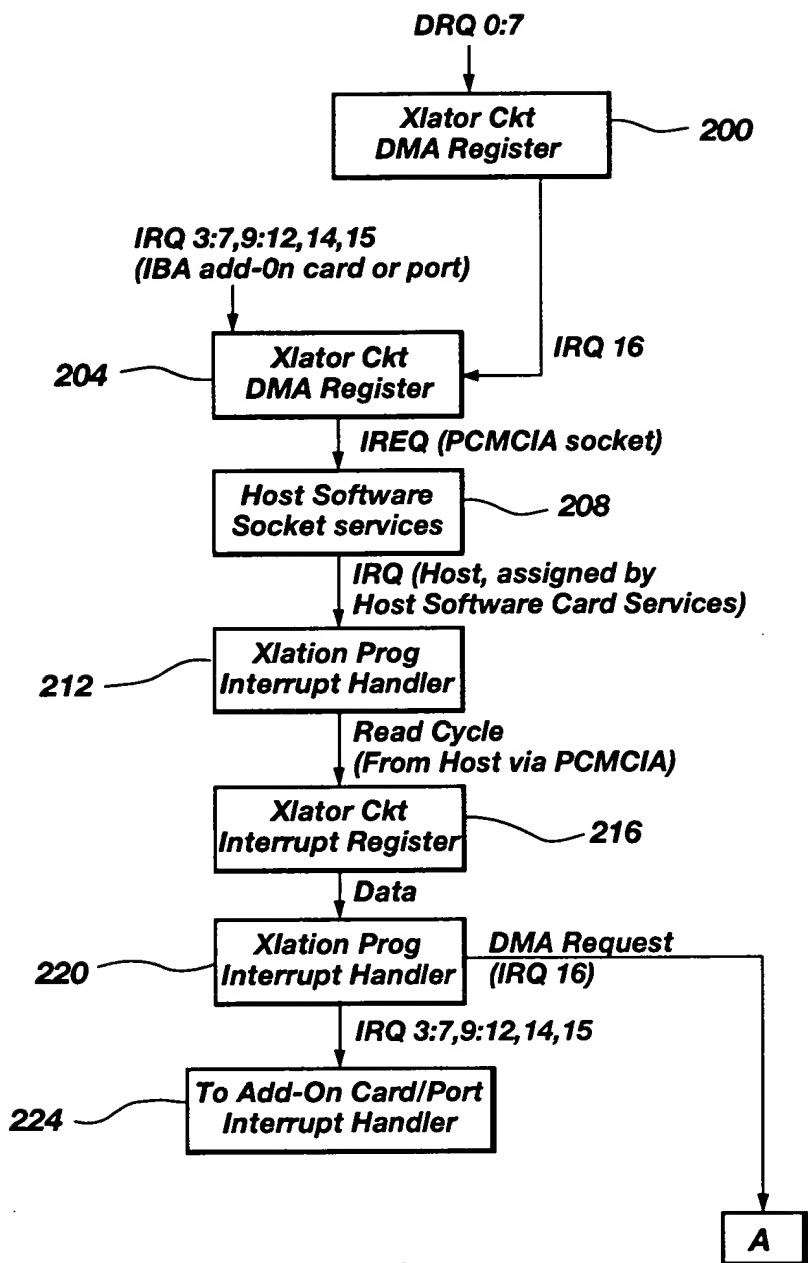


Fig. 4A

APPROVED BY DRAFTSMAN	O.C. FIG.
	CLASS SUBCLASS

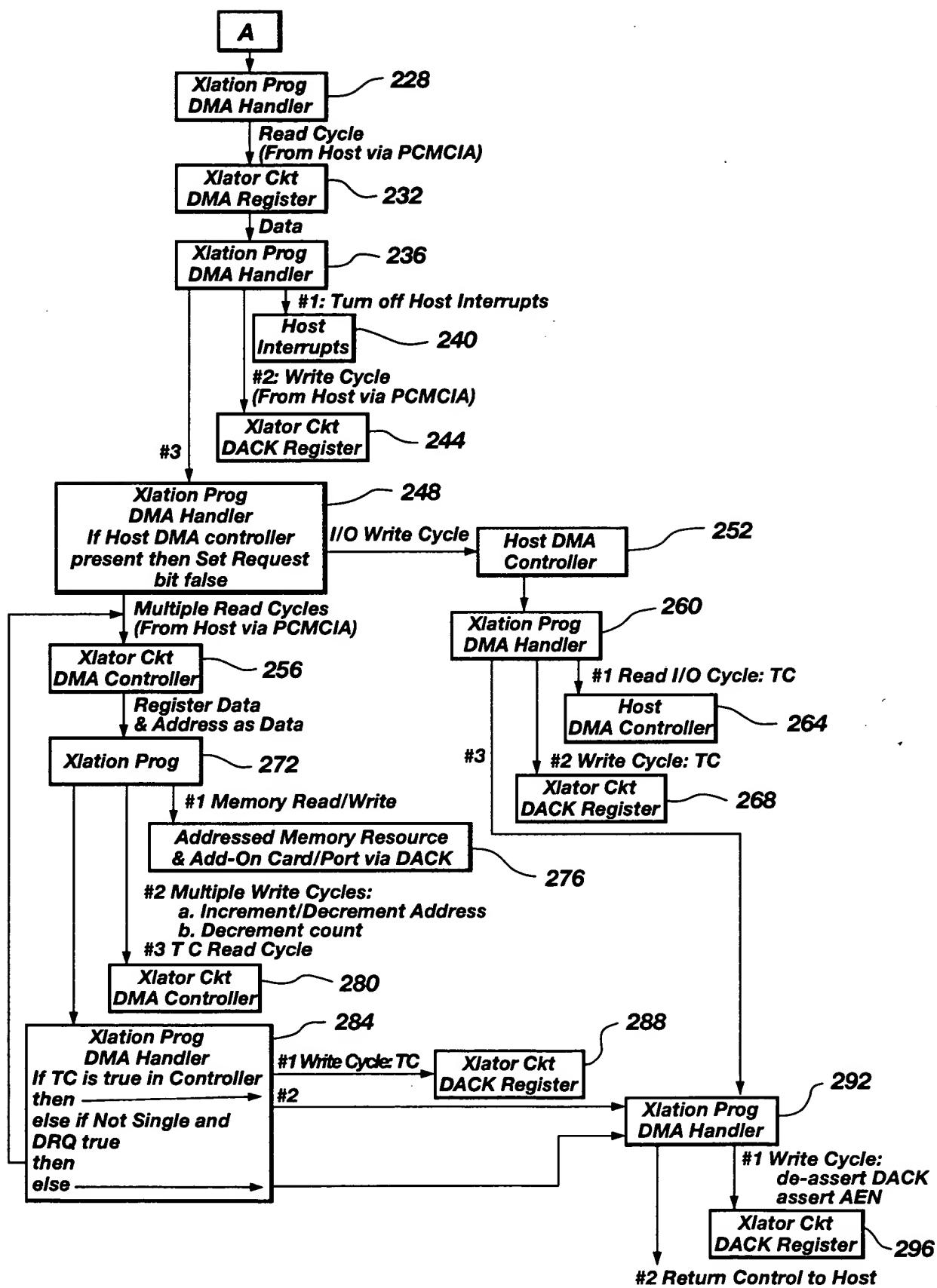
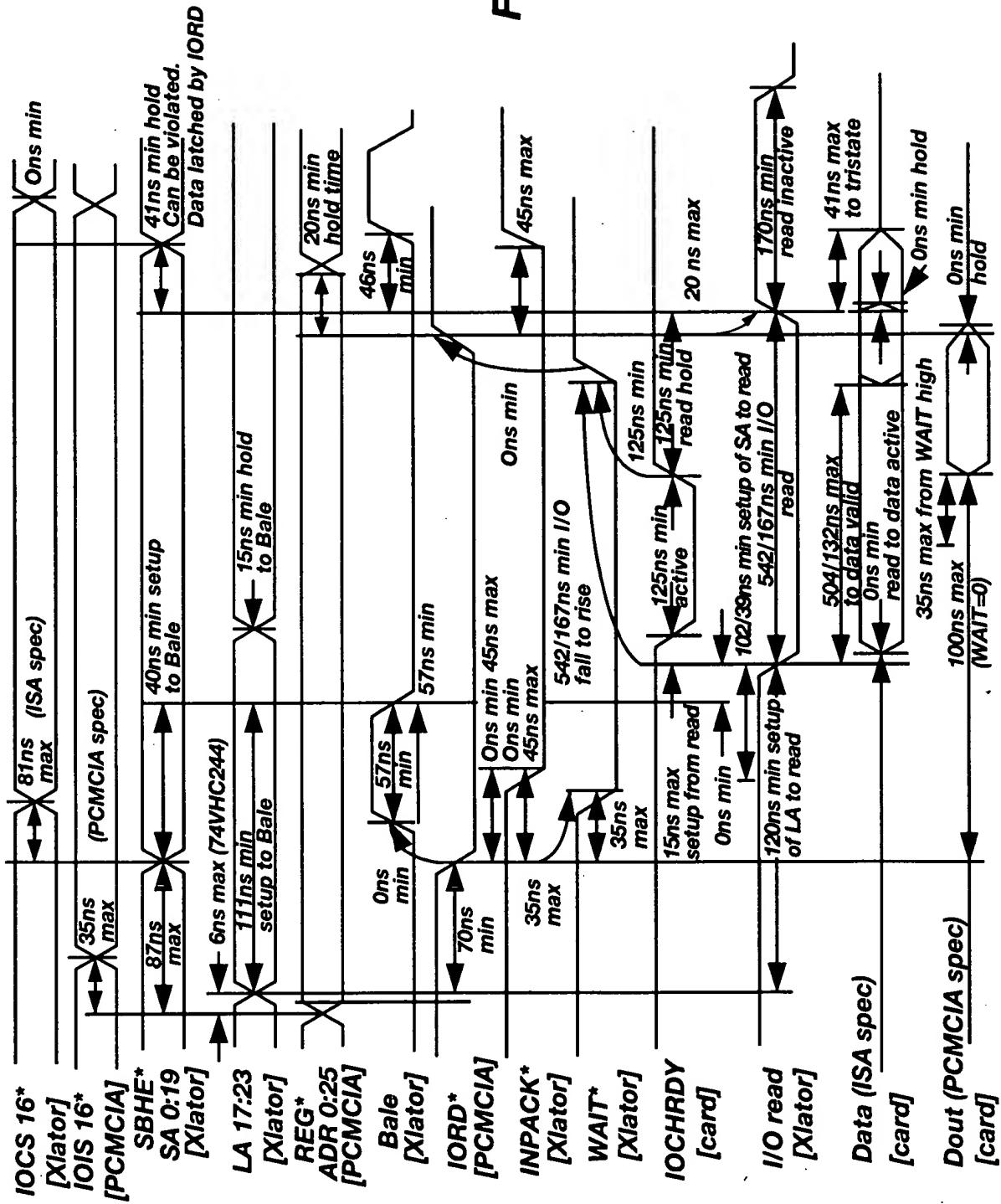


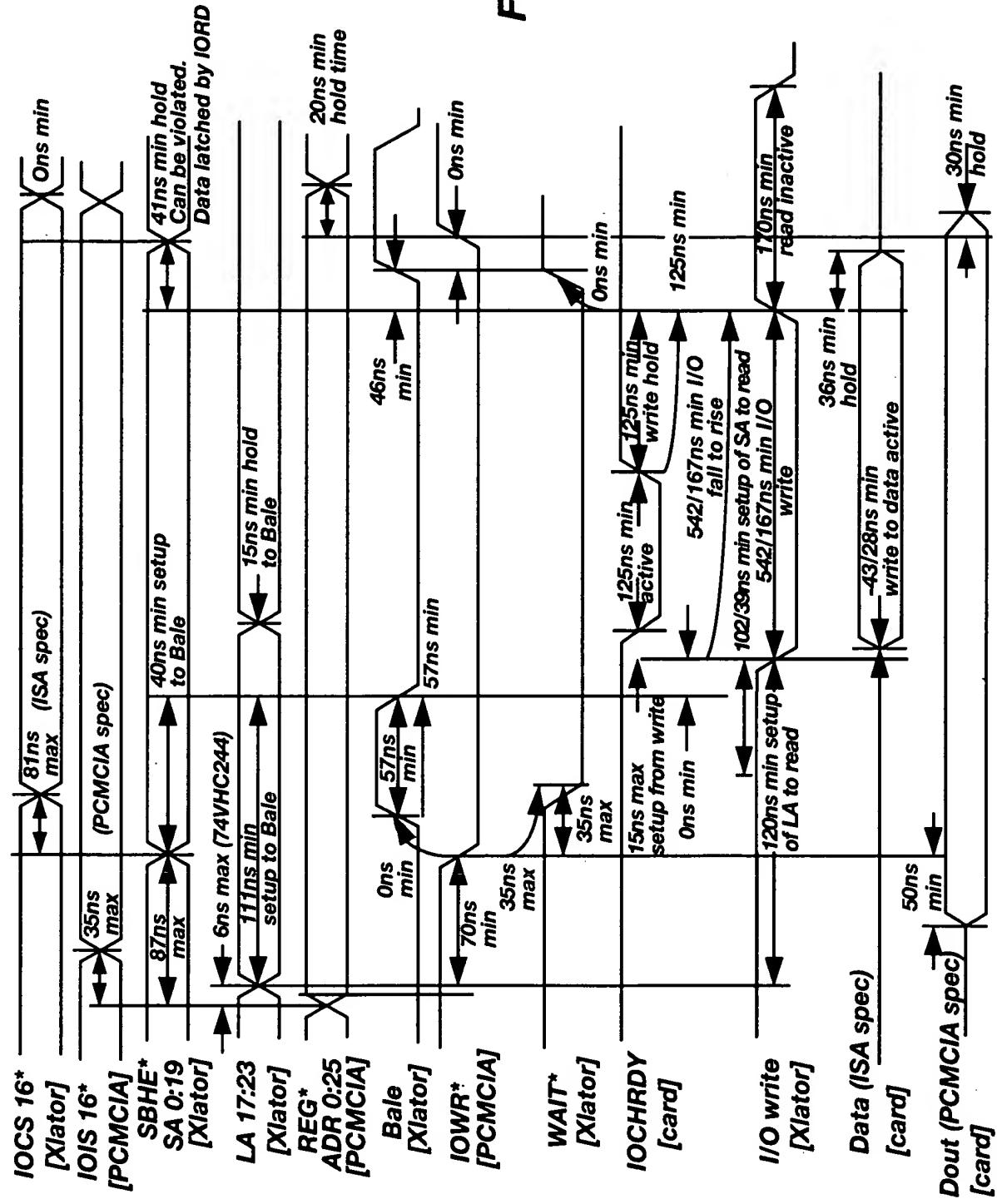
Fig. 4B

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Fig. 5A

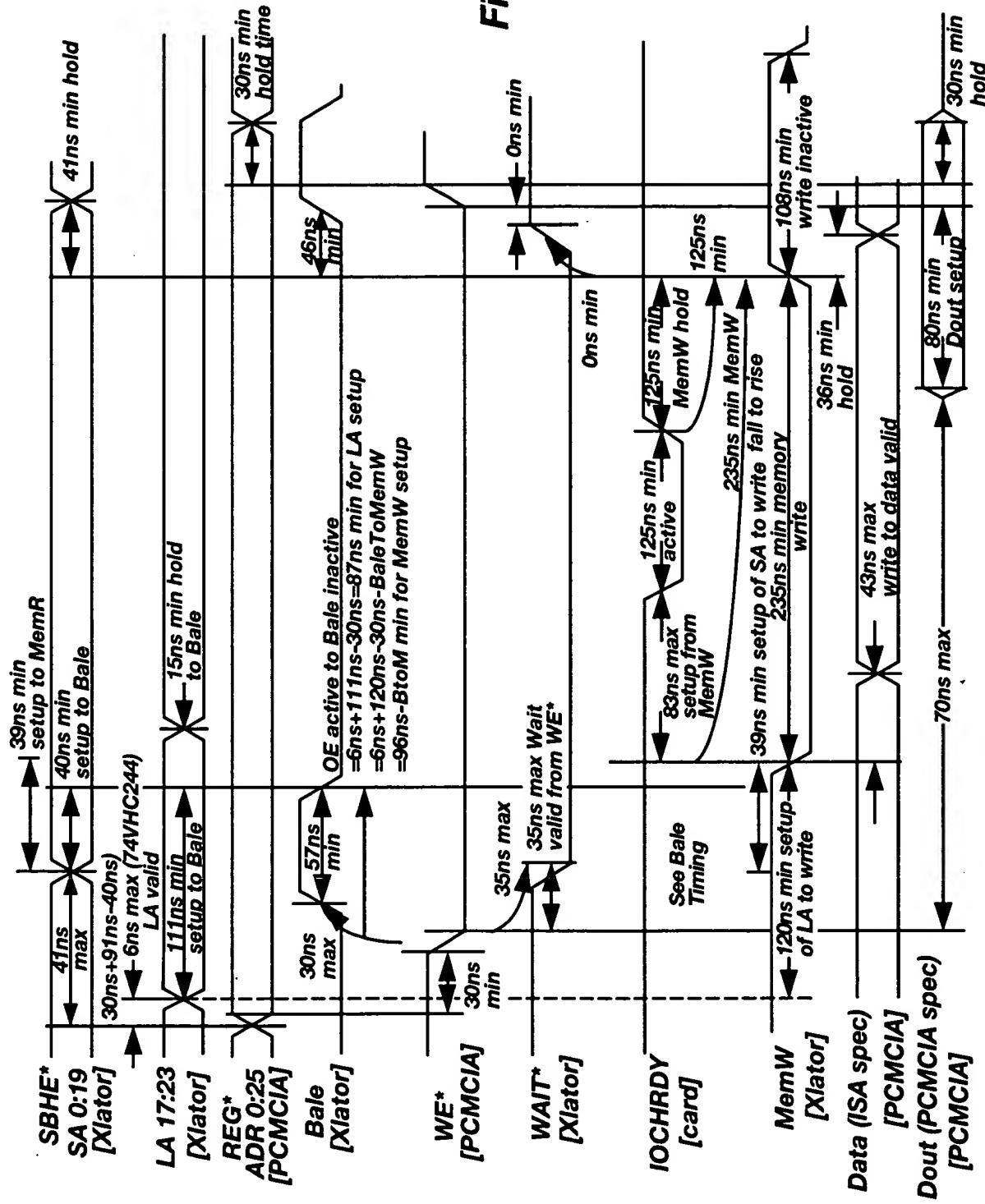


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APPROVED BY DRAFTSMAN	O.G. FIG.
	CLASS SUBCLASS

Fig. 5C

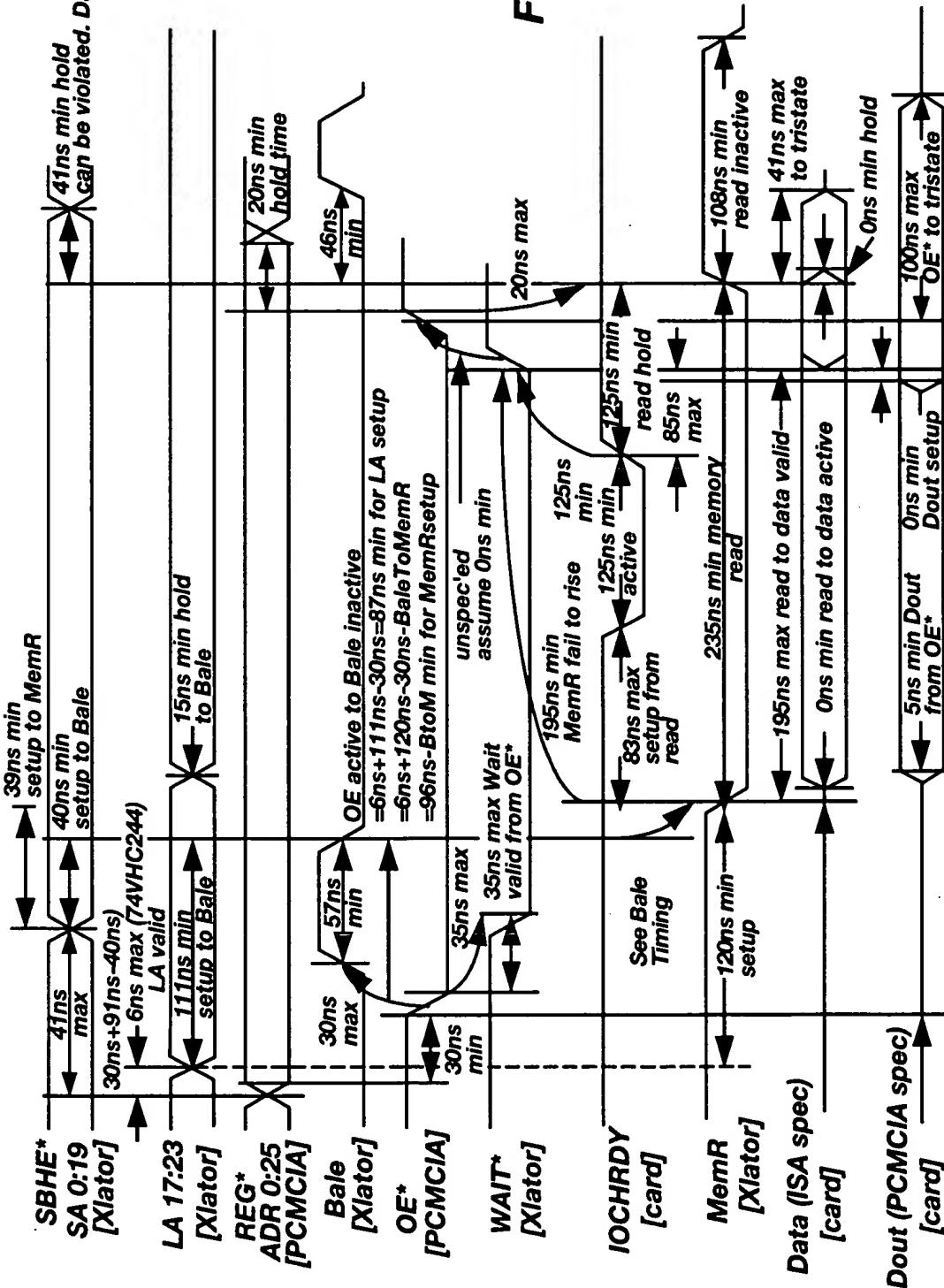


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The timing diagram illustrates the timing requirements for the SBHE* and SA 0:19 signals. The horizontal axis represents time, and the vertical axis represents the logic levels of the signals. The diagram shows the following sequence of events:

- SBHE*** (leftmost signal) is asserted at the start of the timing sequence.
- SA 0:19** (second signal from left) is asserted shortly after SBHE*.
- setup to MemR** (indicated by a double-headed arrow) is the time interval between the assertion of SBHE* and the start of the high period for SA 0:19.
- 41ns max** (indicated by a double-headed arrow) is the maximum time interval between the assertion of SBHE* and the start of the high period for SA 0:19.
- 40ns min setup to Bale** (indicated by a double-headed arrow) is the time interval between the start of the high period for SA 0:19 and the start of the high period for **Bale**.
- 41ns min hold** (indicated by a double-headed arrow) is the minimum time interval between the start of the high period for **Bale** and the end of the high period for SA 0:19.
- OE*** (rightmost signal) is asserted during the high period of SA 0:19.
- 39ns min setup to MemR** (indicated by a double-headed arrow) is the time interval between the end of the high period for OE* and the start of the high period for MemR.
- 41ns min hold** (indicated by a double-headed arrow) is the minimum time interval between the start of the high period for MemR and the end of the high period for OE*.

Fig. 5D



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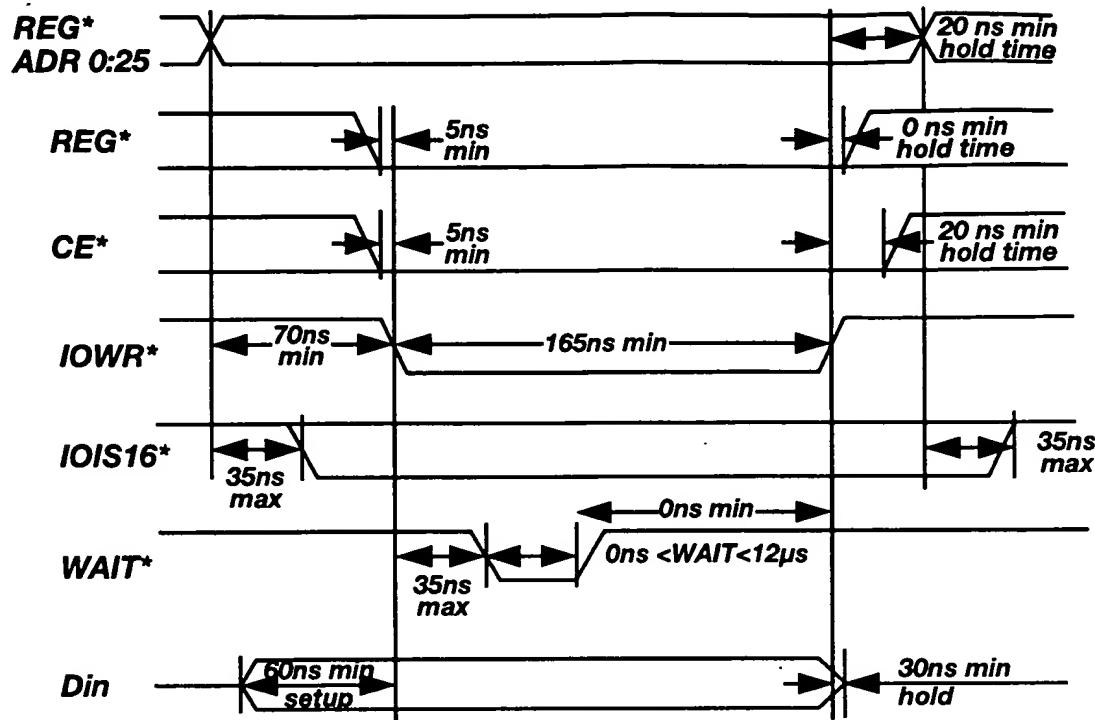


Fig. 5E

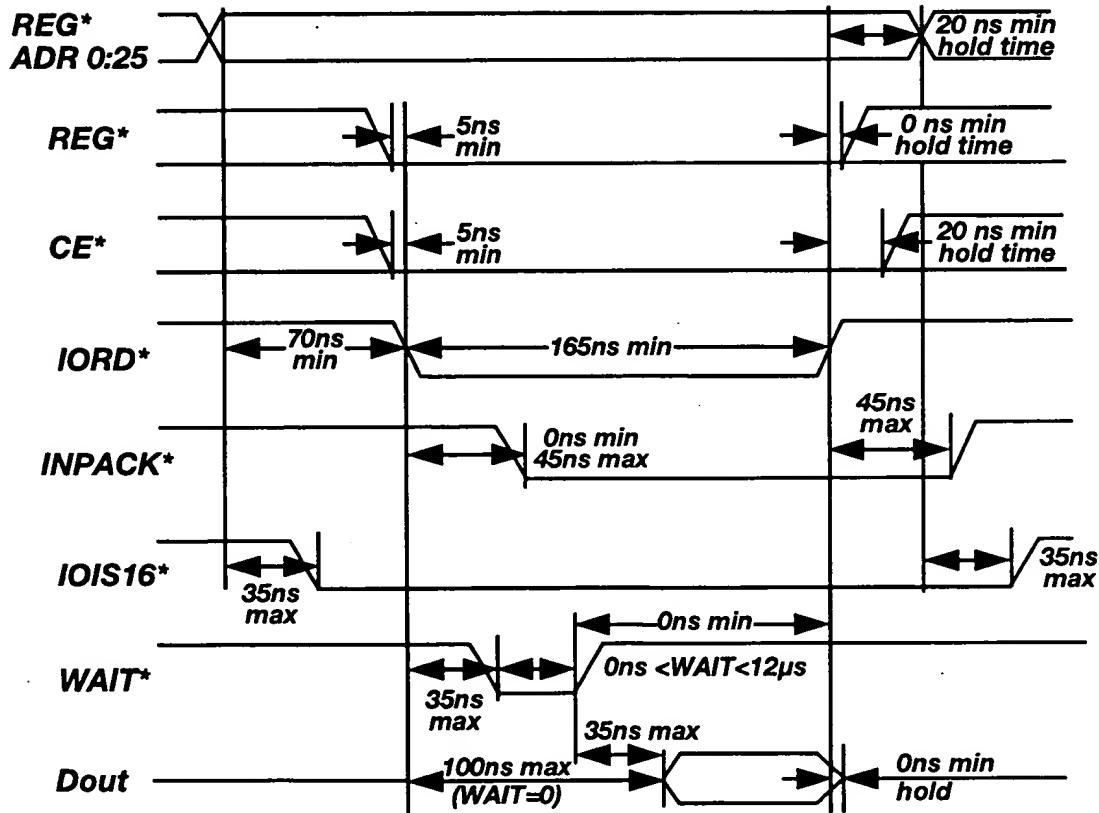


Fig. 5F

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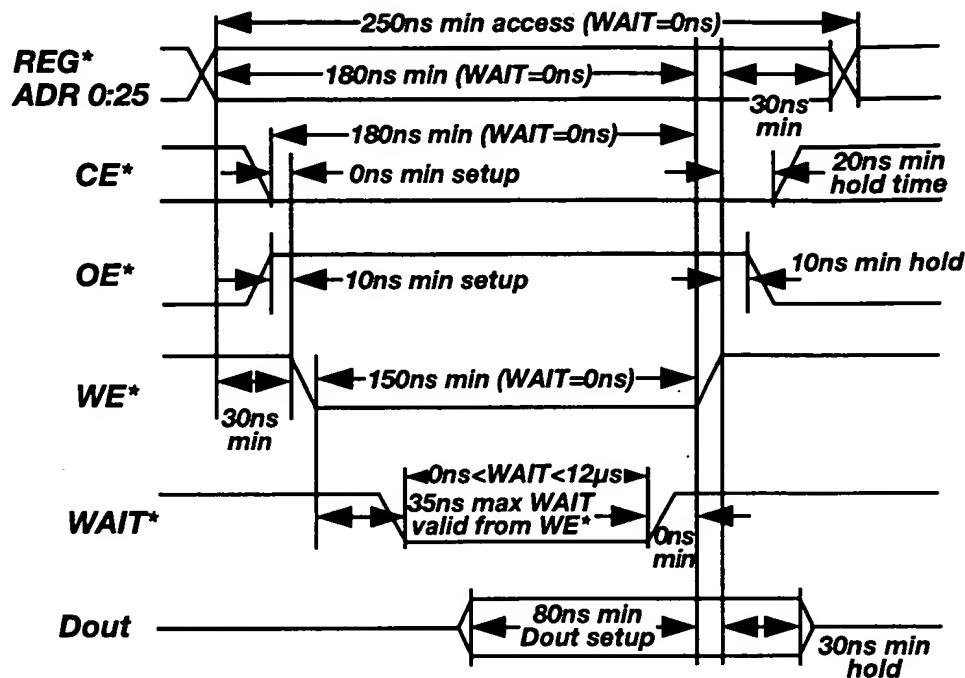


Fig. 5G

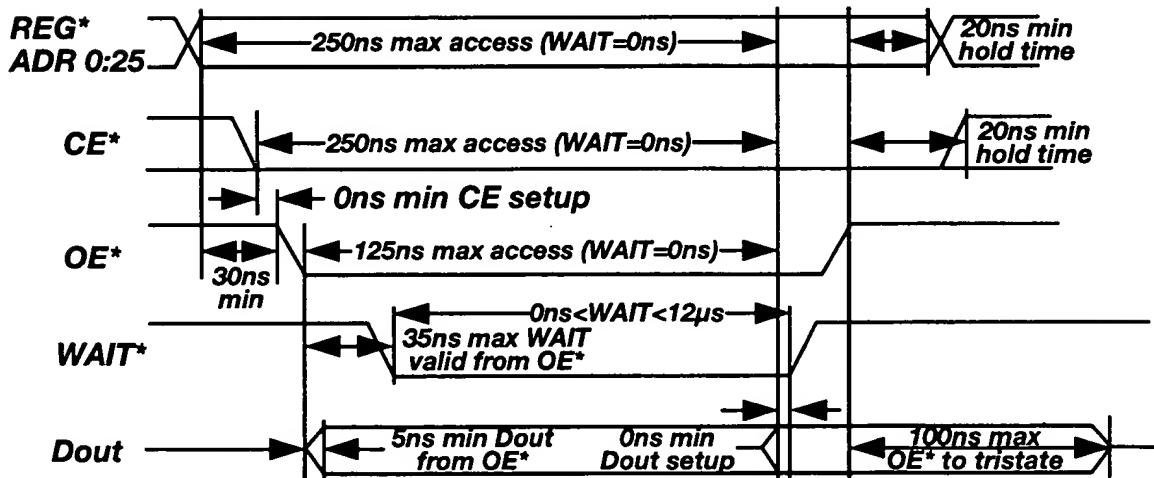


Fig. 5H

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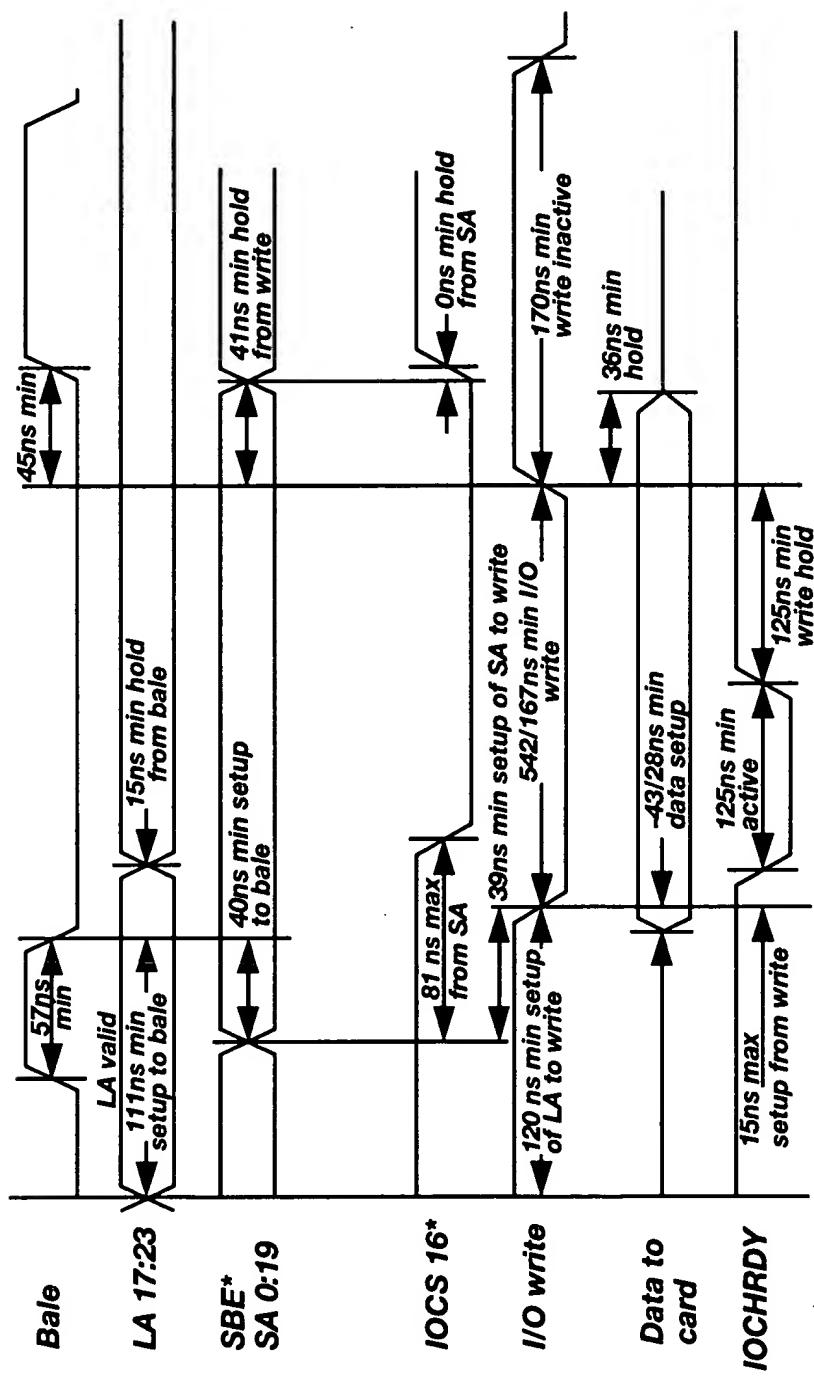


Fig. 51

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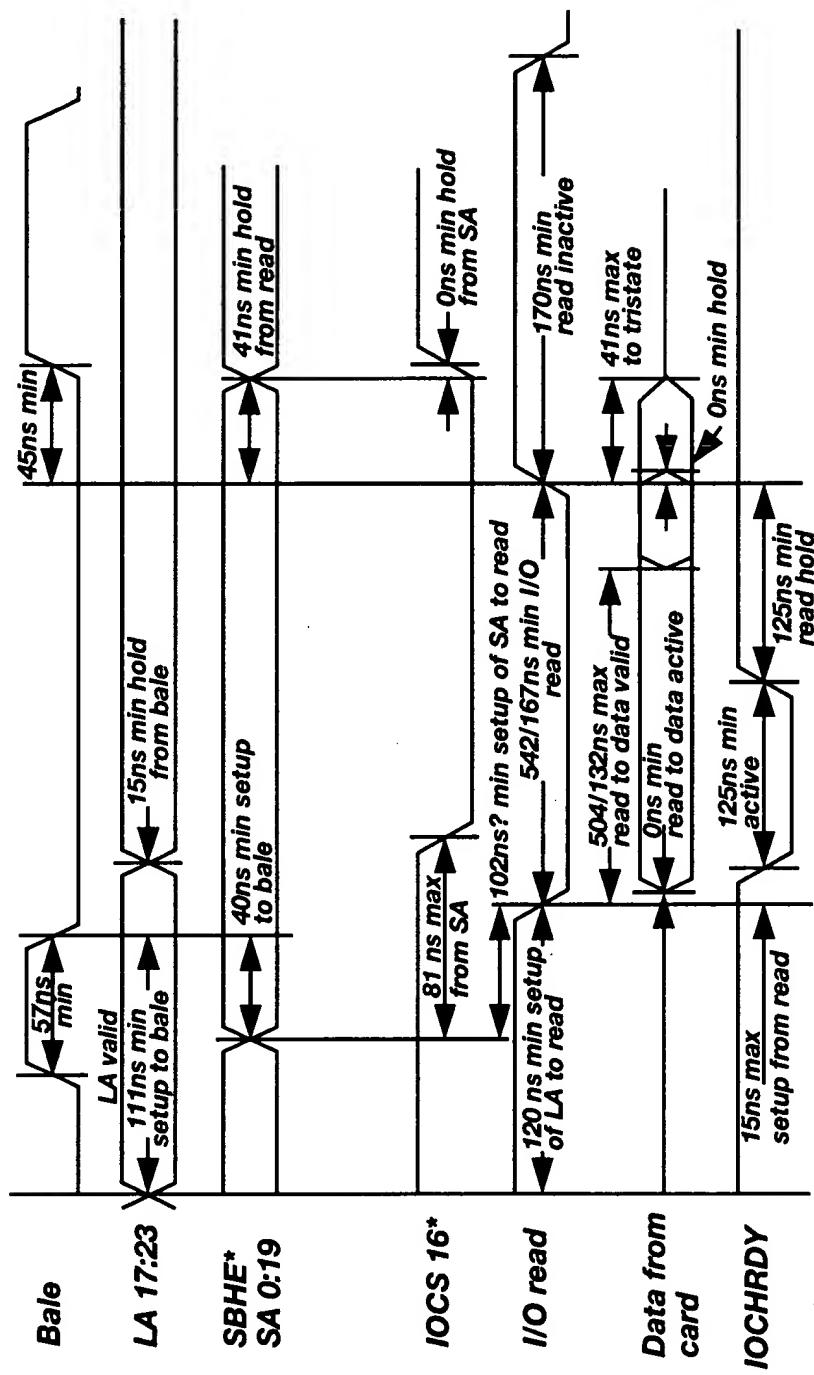


Fig. 5J

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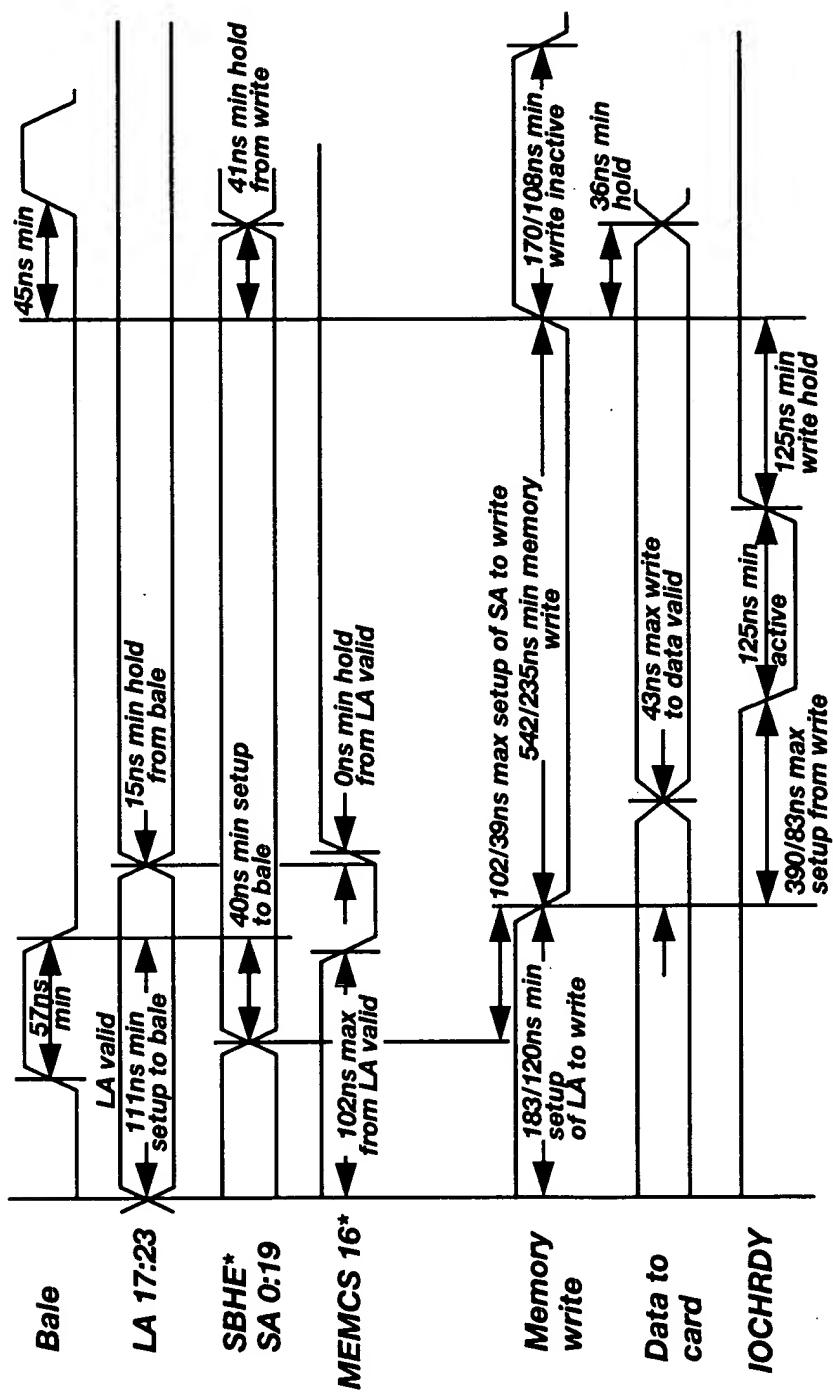


Fig. 5K

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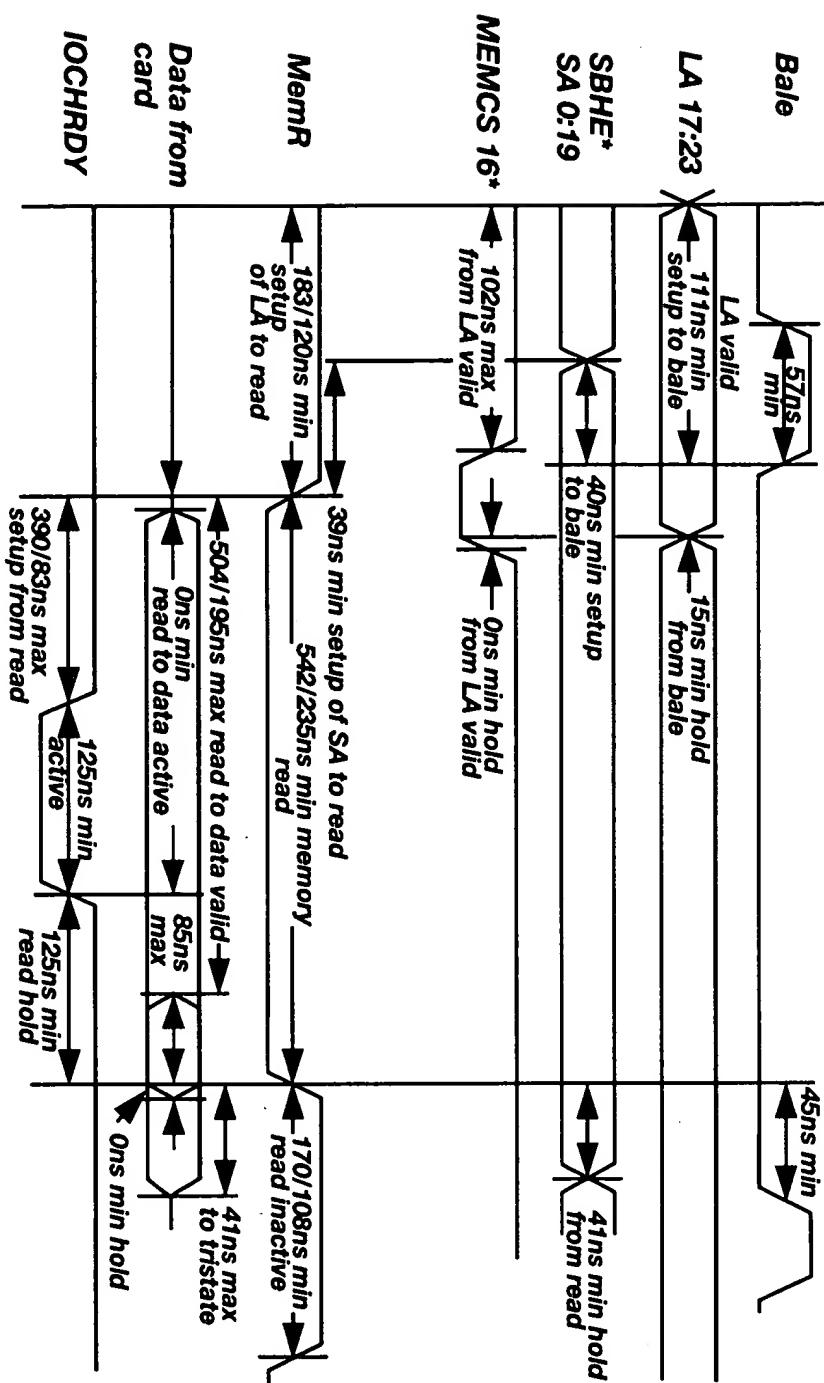


Fig. 5L